

Components and Design Techniques for Digital Systems – CSE 140 (Section 813043)

Basic Information

August 3, 2014

Instructor Alex Orailoğlu

Lecture Mondays 6:00 – 7:50 PM & Tuesdays & Thursdays — 5:00 – 6:50 PM; EBU3B 2154

Discussion Sections Tuesdays, Wednesdays & Thursdays — 7:00 – 7:50 PM; EBU3B 2154

Office Hours Tuesdays & Wednesdays 3:30 – 4:30 PM; EBU3B 3134

Teaching Assistant Ryan Mast

Office Hours Saturdays 1:30 – 3:30 PM; EBU3B B270

Teaching Assistant Josh Marxen

Office Hours Tuesdays 8:30 – 10:00 PM; EBU3B B260A

Tutor Gilbert Lin

Office Hours Tuesdays and Wednesdays — 11:00 AM – 12:30 PM ; EBU3B B260A

Text Book *Fundamentals of Logic Design*, by Charles H. Roth, Jr. and Larry L. Kinney, *Sixth Edition*, Cengage Learning, 2010. Some earlier editions available in the reserve section of the Undergraduate library.

Evaluation	Midterm 1 —	20 %
	Midterm 2 —	25 %
	Final —	40 %
	Homeworks —	15 %

Mid-Term 1 August 14, 2014

Mid-Term 2 August 28, 2014

Final September 6, 2014, 7:00 – 10:00 PM

Assignments Homework problems will be assigned on August 5, 18, and 28; only a selected subset of problems as indicated on the homework will be graded. Homeworks will be due at 4:30 PM on August 13 and 27, and September 4, respectively; you need to turn in your solutions for the homeworks to be graded electronically by the deadline; any homeworks turned in after the deadline will not be read and will receive a grade of zero. You can work in *teams of two* on all homeworks. Solutions to the homework problems will be covered in the discussion sections.

Integrity All work in any exam or quiz must be your own; you must neither copy from nor provide assistance to anybody else. Each student is assumed to be familiar with the *UCSD Policy on Integrity of Scholarship* as described in the UCSD General Catalog. If you have any questions about these policies, be sure to discuss them with the instructor.