

Basic Information

April 10, 2001

Instructor Alex Orailoğlu

Lecture Tuesdays and Thursdays – 12:45 – 2:05 PM; *AP&M* 4218

Office Hours Tuesdays 5:30 – 6:30 PM and Thursdays 2:15 – 3:15 PM;
APM' 4840

Teaching Assistant Kyriakos Lakkas

Office Hours Wednesdays 11:30 AM - 12:30 PM and Fridays 2:30 - 3:30 PM;

Recommended Text Books

- *Synthesis and Optimization of Digital Circuits*, by G. De Micheli, McGraw-Hill, 1994.
- *High-Level Synthesis: Introduction to Chip and System Design*, by D.D. Gajski, N. Dutt, A. Wu, and S. Lin, Kluwer Academic Publishers, 1992.
- *Specification and Design of Embedded Systems*, by D.D. Gajski, F. Vahid, S. Narayan, and J. Gong, Prentice-Hall, 1994.
- *Digital Systems Testing and Testable Design*, by M. Abramovici, M.A. Breuer and A.D. Friedman, IEEE Press, 1990.

Evaluation	Midterm 1 —	20 %
	Midterm 2 —	20 %
	Midterm 3 —	20 %
	Final —	40 %

Mid-Term 1 April 24, 2001

Mid-Term 2 May 15, 2001

Mid-Term 3 May 31, 2001

Final June 15, 2001, 11:30 AM – 2:30 PM

Homework Homework problems will be assigned but will be neither collected nor graded.

Statute of Limitations You have one (1) week from the day any particular graded exam is made available to you to appeal the grade you received. After one week, we will assume that you believe the grade that you got is the correct one. After one week, grades are unchangeable, fixed and permanent.