This homework is based on Cacheing and Virtual Memory concepts described in chapter 7 of the text.

We recommend solving the following problems from the textbook (Patterson and Hennessy, Computer Organization and Design: The hardware software interface, 2nd edition).

**Textbook Problems**

- 7.13, 7.14: Problems on cache hits and misses for various types of caches
- 7.20, 7.21, 7.22: Given a set of address references describe the behaviour and contents of various caches.
- 7.27, 7.28: Performance of various caches.
- 7.32, 7.33: Give you an understanding of virtual memories.