Components and Design Techniques for Digital Systems Laboratory – CSE 140L
(Section 961519)

Basic Information

January 6, 2019

Instructor  Alex Orailoğlu

Lecture  Thursdays — 5:00 - 5:50 PM; HSS 1330

Discussion Section  Thursdays — 6:00 - 6:50 PM; HSS 1330

Office Hours  Mondays 3:30 — 4:30 PM and Thursdays 2:00 — 3:00 PM; EBU3B 3134

Teaching Assistant  Darren Eck and Elbruz Ozen

Tutor  Kevin Huang, Leon Li, Shuyi Ni, Langtian Qin, Alissa Ren and Nick Verchok


Evaluation

<table>
<thead>
<tr>
<th>Lab</th>
<th>Due Date</th>
<th>Percentage</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lab 1</td>
<td>January 10, 2019 → January 18, 2019</td>
<td>7%</td>
</tr>
<tr>
<td>Labs 2, 3, 4, 5</td>
<td>Each lab is worth 17%</td>
<td></td>
</tr>
<tr>
<td>Final</td>
<td>March 21, 2019, 7:00 – 10:00 PM</td>
<td>25%</td>
</tr>
</tbody>
</table>

Lab 1 January 10, 2019 → January 18, 2019

Lab 2 January 17, 2019 → January 28, 2019

Lab 3 January 31, 2019 → February 11, 2019

Lab 4 February 14, 2019 → February 25, 2019

Lab 5 February 28, 2019 → March 11, 2019

Final March 21, 2019, 7:00 – 10:00 PM

Labs  All labs are due Mondays at 9:00 AM on the dates indicated except for the first lab which is still due at 9:00 AM but on Friday, January 18, four days later than regular time. You need to turn in the final report and the associated implementation through gradescope. You can work in teams of two on all labs except for the first which needs to be done individually.

Lab Office Hours  There will be extensive office hour coverage in the labs the week before the labs are due. Tutors will be available in the lab the week before the lab is due on Fridays 5:00 to 7:00 PM, Saturdays 1:00 to 7:00 PM and Sundays 12:00 noon to 10:00 PM. The week before the class meeting when there is no lab due, coverage will be reduced to Fridays 5:00 to 7:00 PM, Saturdays 1:00 to 5:00 PM and Sundays 2:00 to 6:00 PM. We will provide a Google calendar on Piazza so you can follow tutor availability in case you do not parse the schedule mentioned above. You can use the labs in EBU3B in Rooms B250, B260, B270; LogicWorks and Verilog is available on these machines as long as you boot them in Windows mode.
**Exceptional Lab Office Hours** We will hold additional lab office hours on the second week of classes to help you finalize your submission of the first lab which is due on **Friday**, January 18. We will provide the week of January 14th additional lab office hours on Monday, Tuesday, Wednesday and Thursday from 8:00 to 11:00 PM.

**Late Lab Policy** Labs turned in after the 9:00 AM deadline on the designated day are subject to a late lab policy. Late days reduce the total number of points available for the lab by an increasing scale. Namely, 10% of your grade is lost for labs being up to 1 day late, an additional 20% is lost for labs that are late by 2 days, and an additional 30% for labs 3 days late. As you can guess by now, the additional fourth late day would reduce your grade by an additional 40%, making the lab’s evaluation pointless; therefore, no late lab will be accepted 72 hours after the labs are due. Lab 5 cannot be turned in any later than 1 day late; any lab turned in after Tuesday, March 12, 9:00 AM will receive a grade of zero.

**Integrity** All work in the final exam must be strictly your own; all work in any of the labs must be your own or possibly of your teammate (with the exception of the first lab which should be completed individually). You must neither copy from nor provide assistance to anybody else. Each student is assumed to be familiar with the *UCSD Policy on Integrity of Scholarship* as described in the UCSD General Catalog. Be sure to discuss any questions about these policies with the instructor.