

CSE 200 - Winter 2018  
Homework 1  
Due Monday, February 5th, 11:59pm

### **Question 1: Equivalent models of Turing Machines**

A RAM Turing Machine has the following tapes:

- Input tape
- Work tape
- RAM address tape

The input tape is read-only. The rest are both read and write. There are two special instructions (i.e. states) that a RAM Turing Machine has, in addition to a standard Turing Machine.

- **Read from memory:** The content the work tape, at the address given in the RAM address tape, is read and written in the current head location on the work tape.
- **Write to memory:** the content of the work tape, at the address given in the address tape, is rewritten to be the content under the current head location on the work tape.

Both operations do not change the location of the head in the work tape.

- (a) Show that a standard TM (with a read-only input tape, and a read-write work tape) can simulate a RAM TM.
- (b) If the time complexity in the RAM TM is  $T(n)$ , what is the time complexity of its simulation in the standard TM.

*Hint:* Copy the structure of the proof that the number of tapes doesn't matter.

### **Question 2: Rice's theorem**

A property of TMs is a function  $f : \{0, 1\}^* \rightarrow \{0, 1\}$  that satisfies the following condition: for any equivalent TMs  $M_1$  and  $M_2$  (namely, they accept the same inputs, reject the same inputs, and loop on the same inputs) it holds that  $f(\langle M_1 \rangle) = f(\langle M_2 \rangle)$ . Here we assume that  $\langle M \rangle$  is some fixed encoding of TMs.

Prove Rice's theorem: if  $f$  is a computable property of TMs then  $f$  is constant. Namely  $f(\langle M \rangle) = 0$  for all  $M$ , or  $f(\langle M \rangle) = 1$  for all  $M$ .

*Hint:* Reduce from the halting problem.

### **Question 3: Time vs space**

We proved in class that  $SPACE(S) \subset TIME(2^{O(S)})$ . Prove that this exponential gap is tight.

*Hint:* give a problem which can be solved in linear time and logarithmic space.

#### Question 4: below log space

We will prove the following theorem from [Hierarchies of memory limited computations. Stearns, Hartmanis and Lewis. 1969]:

$$SPACE(o(\log \log n)) = SPACE(1)$$

Otherwise put, a TM which uses  $o(\log \log n)$  space can be replaced by another TM which uses  $O(1)$  memory, and hence is equivalent to a Deterministic Finite Automata (DFA) and computes a regular language.

Below is a guided proof, where you will need to prove various claims along the way. Feel free to look at the original paper if you think that it may help you.

Let  $M$  be a TM which has a read-only input tape and a read-write work tape. By assumption,  $M$  uses  $S(n) = o(\log \log n)$  work memory. We assume  $M$  halts on all inputs. We also make a simplifying assumption:  $M$  cannot write blank symbols on the work tape. This implies that at every given point in time, the work tape content is  $w_1 w_2 \dots w_k b b b b \dots$  where  $b$  is a blank symbol, and  $w_1, \dots, w_k$  are non-blank symbols.

For an input  $x \in \{0, 1\}^*$  define  $s(x)$  to be the number of non-blank symbols at the end of the computation of  $M(x)$ . That is,  $s(x)$  is the number of work tape cells used when running  $M$  on  $x$ . By our assumption,  $s(x) \leq S(|x|) = o(\log \log |x|)$  for all inputs  $x$ .

A work configuration is a snapshot of the TM (see page 11 of the textbook), except for the location of the head on the input tape. That is, it includes the content of the work tape, the location of the head in the work tape, and the TM state.

The proof will require us to track all the work configurations obtained while running  $M(x)$ . Let  $W(x)$  denote the set of all work configurations obtained while running  $M(x)$ .

(a) Prove that  $|W(x)| = o(\log n)$  for all  $x \in \{0, 1\}^n$ .

Given input  $x \in \{0, 1\}^n$  and coordinate  $i \in [n]$ , define  $W(x, i) \subset W(x)$  to be the set of all the work configurations obtained when running  $M(x)$ , when the input tape head happened to be at coordinate  $i$ . Observe that  $w \in W(x, i)$  together with  $i$  give a complete snapshot of the TM.

(b) Prove that for any  $x \in \{0, 1\}^n$ , the number of distinct  $W(x, i)$  as  $i$  ranges  $1..n$  is  $o(n)$ .

That is,  $|\{W(x, i) : i = 1, \dots, n\}| = o(n)$ . Note that this is better than the trivial bound of  $n$ .

(c) Prove that there exists  $n_0 \geq 1$  such that: for any  $n \geq n_0$  and any  $x \in \{0, 1\}^n$ , there exist  $i < j$  for which  $W(x, i) = W(x, j)$  and  $x_i \neq x_j$ .

Let  $B$  denote the maximum amount of memory used by any input of length  $\leq n_0$ . That is,  
 $B = \max\{s(x') : x' \in \{0, 1\}^{n'}, n' \leq n_0\}$ .

- (d) Prove that unless  $M$  uses  $O(1)$  memory for all inputs, there must be input length  $n > n_0$  and an input  $x \in \{0, 1\}^n$  for which  $s(x) > B$ .

Fix such  $n$  and  $x \in \{0, 1\}^n$ . By (c), there exist coordinates  $i < j$  for which  $W(x, i) = W(x, j)$  and  $x_i = x_j$ . Define a new word:

$$y = x_1 \dots x_i x_{j+1} \dots x_n \in \{0, 1\}^{n-j+i}.$$

To conclude the proof, we will show that  $M(y)$  never halts, which is a contradiction, as  $M$  should halt on all inputs.

- (e) Prove that  $W(y, k) \subseteq W(x, k)$  for  $k = 1, \dots, i$ ; and  $W(y, i+k) \subseteq W(x, j+k)$  for  $k = 1, \dots, n-j$ .

*Hint:* reason by induction on the number of steps in the computation. The only places where the work configuration of  $M(y)$  and  $M(x)$  might diverge is in the "crossover" between  $x_i$  and  $x_{j+1}$ . Explain why the condition in (c) guarantees that it doesn't.

- (f) Prove that  $s(y) < s(x)$ .

- (g) Prove that  $M(y)$  never reaches a work configuration where the state is an accept or a reject state.

- (h) Complete the proof.

Advice: if you get stuck at stages (g)-(h), read the original paper. It is explained well there.