1. **What is a TLB and what does it do?**
   1. The TLB is a cache for the translation between virtual and physical addresses
   2. It stands for translation lookaside buffer

2. **Which provides faster access, a virtual cache or a physical cache?**
   1. Bad question. Everyone gets this one right.
   2. My answer: The physical cache is slower because you have access to the TLB first.

3. **Is PIO fast or slower than DMA for large data transfers?**
   1. PIO is slower for large transfers

4. **What is an interrupt?**
   1. An interrupt allows the processor to service a request from an IO device. It immediately transfers control into the kernel.

5. **What is the advantage of moving an IO device from off-chip to on-chip?**
   1. On-chip communication is lower latency and higher bandwidth.
   2. It also reduces costs, because it reduces the number of chips in the system.