Micro Project #1: “Stupid” ISA Tricks
Due 2/3/11
Swith-a-roo

• Consider the following ISA
  • add, sub, mul, or, xor, nand
  • 2 general purpose registers R1 and R2.
  • No memory.

• Give the MIPS-style assembly for swapping the contents of R1 and R2
Turning it in

- Email them to me (not the TAs)
- Subject: [cse141 Microproject 1] in the subject.
- If you get the subject wrong, no guarantees.