Midterm II Conceptual Summary
Sample Questions(1)

- What is instruction level parallelism?
- Describe the basic 4-state branch predictor used as the basis for many of the predictors we examined in class?
- What is register renaming? Why is it useful?
- Given a sequence of instruction, identify the RAW, WAR, and WAW dependencies.
- Which of RAW, WAW, and WAR are false dependencies as why?
- Simulate a short sequence of code on a machine that uses Tomasulo’s algorithm.
Sample Questions (2)

- What defines a thread of execution in a von Neumann processor?
- Identify the potential memory dependences with a short sequence of code.
- What structure in an OOO processor is responsible for identifying and resolving actual memory dependences?