

First Name:

Last Name:

PID:

4. (10 points)

An FSM is defined by the following state transition table.

Present State $Q_1 Q_0$	Next state		Output Z
	$W=0$	$W=1$	
	$Q_1^+ Q_0^+$	$Q_1^+ Q_0^+$	
00	10	11	0
01	00	00	0
10	10	00	0
11	10	00	1

(a) Derive the next state and output equations.

Q_1, Q_0	00	01	11	10
W				
0	1		1	1
1	1			

K-map for Q_1^+

$$Q_1^+ = \overline{Q_1} \overline{Q_0} + Q_1 \overline{W}$$

Q_1, Q_0	00	01	11	10
W				
0		0		0
1	1		0	

K-map for Q_0^+

$$Q_0^+ = \overline{Q_1} \overline{Q_0} W$$

→ Since this is a Moore machine, output depends only on current state

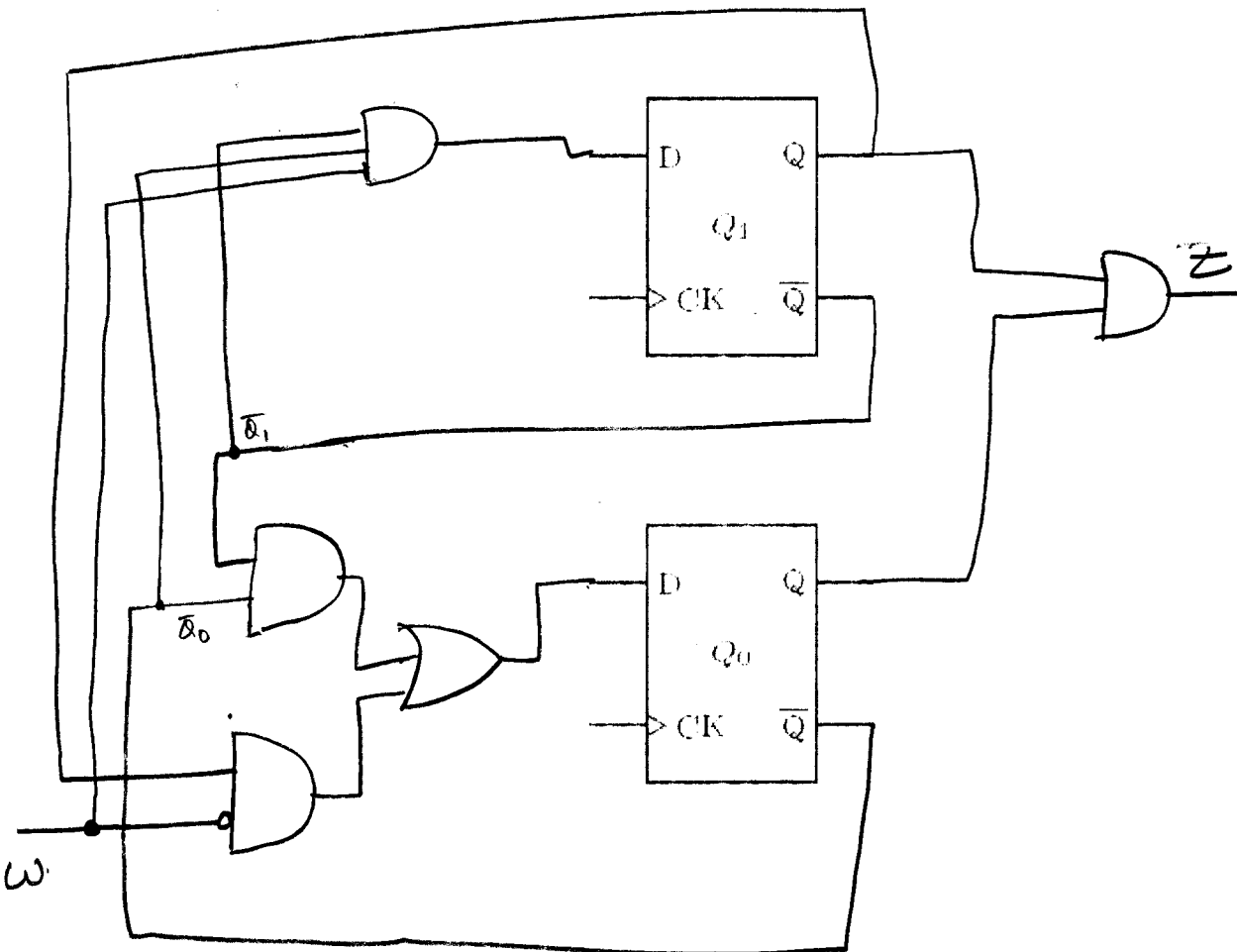
$$\underline{\underline{Z = Q_1 Q_0}}$$

First Name:

Last Name:

UID:

(b) Provide the logic schematic of this machine using only D flip-flops, AND gates, OR gates, and NOT gates.

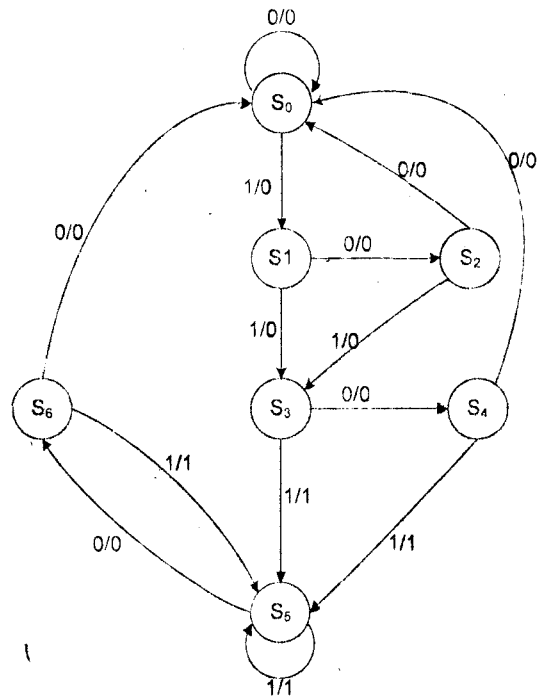


First Name:

Last Name:

PID:

5. Consider the following state diagram:



(a) Fill out the blanks in the state table

Present State	Next State		Output Y	
	A = 0	A = 1	A = 0	A = 1
S ₀	S ₀	S ₁	0	0
S ₁	S ₂	S ₃	0	0
S ₂	S ₀	S ₃	0	0
S ₃	S ₄	S ₅	0	1
S ₄	S ₀	S ₅	0	1
S ₅	S ₆	S ₅	0	1
S ₆	S ₀	S ₅	0	1

First Name:

Last Name:

PID:

(b) Label the rows and columns with states, and then fill the implication table.

s_1	s_0-s_2 s_1-s_3					
s_2	s_0-s_0 s_1-s_3	s_2-s_0 s_3-s_3				
s_3	X	X	X			
s_4	X	X	X	s_4-s_0 s_5-s_5		
s_5	X	X	X	s_4-s_6 s_5-s_5	s_0-s_6 s_5-s_5	
s_6	X	X	X	s_4-s_0 s_5-s_5	s_0-s_0 s_5-s_5	s_0-s_6 s_5-s_5
	s_0	s_1	s_2	s_3	s_4	s_5

(c) List all the original states, grouped into maximal classes of compatibility. Use only as many groups as necessary.

$$g_0 = \underline{s_0 -}$$

$$g_1 = \underline{s_1 -}$$

$$g_2 = \underline{s_2 -}$$

$$g_3 = \underline{\{s_3, s_5\} -}$$

$$g_4 = \underline{\{s_4, s_6\}}$$

$$g_5 = \underline{\hspace{2cm}}$$

$$g_6 = \underline{\hspace{2cm}}$$

First Name:

Last Name:

PID:

(d) Show the minimized state transition and output table. Fill in only as many rows of the table as necessary.

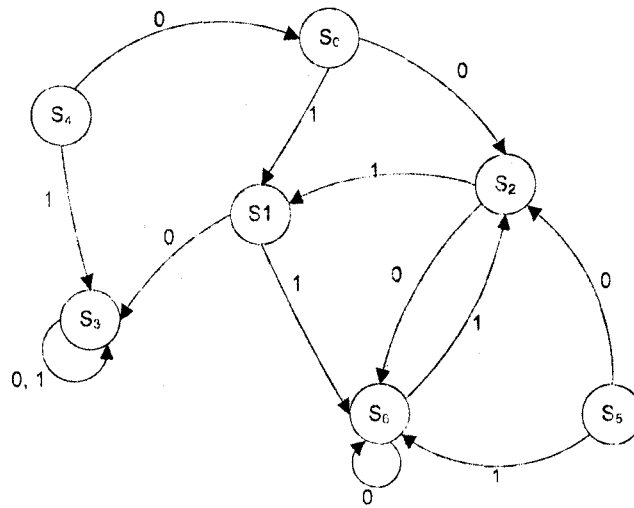
Present State	Next State		Output Y	
	$\Lambda = 0$	$\Lambda = 1$	$\Lambda = 0$	$\Lambda = 1$
g_0	g_0	g_1	0	0
g_1	g_2	g_3	0	0
g_2	g_0	g_3	0	0
g_3	g_4	g_3	0	1
g_4	g_0	g_3	0	1
g_5				
g_6				

First Name:

Last Name:

PID:

6. Consider the finite state machine shown below.



Implement a state assignment using the minimum bit-change heuristic. Show your result in both K-map and the table.

	00	01	11	10
0	S ₀	S ₂		S ₄
1	S ₁	S ₆	S ₅	S ₃

State Name	Assignment		
	Q ₂	Q ₁	Q ₀
S ₀	0	0	0
S ₁	0	0	1
S ₂	0	1	0
S ₃	1	0	1
S ₄	1	0	0
S ₅	1	1	0
S ₆	0	1	1