**Synchronization**

Synchronize: to (arrange events to) happen at same time

Process synchronization
- When one process has to wait for another
- Events in processes that occur “at the same time”

Uses of synchronization
- Prevent race conditions
- Wait for resources to become available

**Credit/Debit Problem: Race Condition**

Say $P_1$ runs first
- read $1000$ into $b$
- switch to $P_0$
- read $1000$ into $b$
- debit by $900$
- write $900$

Switch to $P_1$

$P_0$
- credit (int $a$)
  - int $b$
  - $b = \text{ReadBalance} ()$
  - $b = b + a$
  - $\text{WriteBalance} (b)$
  - $\text{PrintReceipt} (b)$

$P_1$
- debit (int $a$)
  - int $b$
  - $b = \text{ReadBalance} ()$
  - $b = b - a$
  - $\text{WriteBalance} (b)$
  - $\text{PrintReceipt} (b)$

Switch to $P_0$
- credit by $100$
- write $1100$

Bank just lost $100$!
To Avoid Race Conditions

Identify critical sections
- Sections of code executed by multiple processes
- Must run atomically with respect to each other

Enforce mutual exclusion
- Only one process active in a critical section

Four Rules for Mutual Exclusion
1. No two processes inside critical sections at same time
2. No process outside critical section may block others
3. No process may wait forever to enter critical section
4. No assumptions about speeds or number of CPU's

How to Achieve Mutual Exclusion?
Surround critical section with entry/exit code

Entry code should act as a barrier
- If another process is in critical section, block
- Otherwise, allow process to proceed

Exit code should act to release other entry barriers

Possible Solution: Software Lock?
Lock indicates whether any process is in critical section

Race condition in while loop (breaks Rule 1)
Possible Solution: Take Turns?

Alternate which process can enter critical section

Prevents entry even if OK to enter (breaks Rule 2)

Possible Solution: State Intention?

Each process states it wants to enter critical section

Race condition: prevents entry forever (breaks Rule 3)

Peterson’s Solution

If there is competition, take turns; otherwise, enter

Works! Extends to n processes. Problem: busy-waiting

What About Disabling Interrupts?

By disabling interrupts, context-switching can't occur

Too restrictive (breaks Rule 2)
  • locks out all processes
  • even those not in critical section

Doesn’t work on a multiprocessor (breaks Rule 4)
**Hardware Solution: TSET Instruction**

Hardware instruction: TSET mem
- simultaneously test mem and set it to 1
- \{ reg ← mem, mem ← 1, set cond code \} atomically

C function: `tset (lock)` sets lock to 1, returns orig value

```c
shared int lock = 0;
P0 while ( tset (lock) == 1 );
< critical section >
lock = 0;
P1 while ( tset (lock) == 1 );
< critical section >
lock = 0;
```

Simple, works for n processes (but still busy-waits!)

**Semaphores**

Semaphore: synchronization variable
- Has integer value
- List of waiting processes

Works like a gate
- If sem > 0, gate is open
  - value indicates number of processes that can enter
- Else, gate is closed
  - possibly with waiting processes

```c
sem = 1
sem = 2
sem = 3
sem = 0
```

**Semaphore Operations**

```c
sem s = n;        /* declare and initialize */
wait (sem s)
• Decrement s
• If s < 0, block process (and associate with s)
signal (sem s)
• Increment s
• If blocked processes, unblock (any) one of them

No other operations permitted (e.g., can’t test value)
```

**An Incorrect Definition**

```c
sem s = n;        /* declare and initialize */
wait (sem s)
• If s == 0, block process (and associate with s)
• Decrement s (note: occurs after process unblocks)
signal (sem s)
• Increment s
• If blocked processes, unblock (any) one of them

WHERE IS THE RACE CONDITION?
```
Mutual Exclusion with Semaphores

Use a "mutex" semaphore initialized to 1

\[
\text{sem mutex} = 1
\]

\[
\begin{align*}
\text{P}_0 & : \text{wait (mutex)}; \quad \text{P}_1 : \text{wait (mutex)}; \\
& \quad \text{< critical section >} \quad \text{< critical section >} \\
& \quad \text{signal (mutex)}; \quad \text{signal (mutex)};
\end{align*}
\]

Allows only one process to enter critical section

Simple, works for \( n \) processes, no busy-waiting (really?)

Synchronization with Semaphores

Use a "synch" semaphore initialized to 0

\[
\text{sem synch} = 0
\]

\[
\begin{align*}
\text{P}_0 & : \text{signal (synch)}; \\
\text{P}_1 & : \text{wait (synch)}; \quad \text{< to be done after } \text{P}_0 > \\
& \quad \text{< to be done before } \text{P}_1 > \\
& \quad \text{signal (synch)};
\end{align*}
\]

Allows a process to wait for another before proceeding

Semaphores provide pure synchronization

- No way for a process to tell it blocked
- I.e., no information transfer

Atomicity of Semaphore Operations

wait (s) and signal (s) are atomic operations

- Their bodies are critical sections

Mutual exclusion achieved using a lower-level mechanism

- Test-and-set locks
- Disabling interrupts (on a uniprocessor)

Therefore, problems such as busy-waiting still exist

- But at a "lower-level"
- For brief (and known) periods of time