CSE 243A (Section Id: 947925)
VLSI Computer-Aided Design
Synthesis Methodologies in VLSI Design & Test

Conference Reading List

Professor Alex Orailoğlu
EBU3B 3134
Tel: 858-534-0914 Fax: 858-534-7029
email: alex@cs.ucsd.edu


• Matthew M. Ziegler, George Gristede and Victor V. Zyuban. “Power reduction by aggressive synthesis design space exploration” International Symposium on Low Power Electronics and Design (ISLPED), Beijing, China, September 2013, pp. 421–426. Darren


