VLSI Computer-Aided Design — CSE 243A (Section 947925)

Basic Information

October 1, 2018

Instructor Alex Orailoğlu

Lecture Mondays and Wednesdays – 5:00 – 6:20 PM; EBU3B 4140

Office Hours Mondays and Wednesdays 3:30 – 4:30 PM; EBU3B 3134

Tutor Leon Li

Office Hours Tuesdays 3:30 – 4:30 PM; EBU3B B250A

Main Text Book


Recommended Text Book


Evaluation

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<tbody>
<tr>
<td>Midterm</td>
<td>30%</td>
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<tr>
<td>Paper Reviews</td>
<td>25%</td>
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<tr>
<td>Final</td>
<td>45%</td>
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Mid-Term November 14, 2018

Final December 13, 2018, 7:00 – 10:00 PM

Homework Homework problems will be assigned but will be neither collected nor graded; solutions will be made available to students.

Paper Reviews Students will conduct class presentations of papers. Each paper presentation will take approximately 10 minutes. Additionally, students will make written reviews and critiques of a number of papers during the quarter. The papers to be reviewed will cover recent research in High Level Synthesis. A list of eligible papers in the area will be provided in the third week of classes.

Statute of Limitations You have one (1) week from the day any particular graded exam is made available to you to appeal the grade you received. After one week, we will assume that you believe the grade that you got is the correct one. After one week, grades are unchangeable, fixed and permanent.