CSE 243A (Section Id: 820328))
VLSI Computer-Aided Design
Synthesis Methodologies in VLSI Design & Test

Reading List

Professor Alex Orailoglu
EBU3B 3134
Tel: 858-534-0914 Fax: 858-534-7029
email: alex@cs.ucsd.edu

• Teich, J., Hardware/software codesign: The past, the present, and predicting the future, in Proceedings of the IEEE, Volume 100, Issue 13, May 2012, Page(s):1411-1430.


• Chengmo Yang, Mingjing Chen, Alex Orailoglu, "Squashing microcode stores to size in embedded systems while delivering rapid microcode accesses," in Design Automation for Embedded Systems, Volume 14, Issue 3, September 2010, Page(s) 265 - 284.
