CSE 240A (Section Id: 756003)
Principles of Computer Architecture
MoWe 5:00 - 6:20 PM; WLH 2113

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Course Contents


**Limits on Instruction Level Parallelism** Studies on Limitations of ILP. Multithreading.

**Memory-Hierarchy Design** Basics of Cache Performance. Reducing Cache Miss Penalty, Miss Rate, Hit Time.

**Application Specific Processors** Cache Customizations. Low-Power Application Specific Communication Techniques. Power Issues in Mobile Processors.