

Micro Project #2

Due October 29th

Where am I?

- Without using conditional branches, pseudo instructions, or more than one immediate-mode instruction, fill in the “<your code here>” below so that register \$s1 contains the address of the instruction a label “here”. ‘r’ can be any register you choose.

```
    <your code here>  
    add    $s1, r, zero  
here:    ...
```