

**Micro Project #1:
“Stupid” ISA Tricks
Due 10/15/09**

Swith-a-roo

- Consider the following ISA
 - add, sub, mul, or, xor, nand
 - 2 general purpose registers R1 and R2.
 - No memory.
- Give the MIPS-style assembly for swapping the contents of R1 and R2