CSE 240 (Section Id: 631684)
Principles of Computer Architecture
TuTh 12:30 - 1:50 PM; WLH 2111

Professor Alex Orailoglu
EBU3B 3134
Tel: 858-534-0914 Fax: 858-534-7029
email: alex@cs.ucsd.edu

Course Contents


**Limits on Instruction Level Parallelism** Studies on Limitations of ILP. Multithreading.

**Memory-Hierarchy Design** Basics of Cache Performance. Reducing Cache Miss Penalty, Miss Rate, Hit Time.