Grading policy
CSE 140 Final

Q. 1)

a (12 points)
• -3 points for each incorrect row or column
• -3 for each missing column

b (13 points)
• -2 for incorrect J1K1 column in truth table
• -2 for incorrect J0K0 column in truth table
• -2 for each incorrect K-map for J and K
• -2 for any incorrectness in circuit
• -6 for using incorrect mux’s or any other gate

Q. 3)

a (12 points)
• -4 points for using any gate other than XOR (assuming circuit is correct)
• -4 for incorrect Cout, -4 for incorrect Sum in the final circuit

b (13 points)
• -2 points for each incorrect entry in the truth table (max up to 3 incorrect)
• -2 for each incorrect K-map (should be 2 K-Maps – one for J and one for K)
• -2 for some error in the schematic diagram
• -4 for each incorrect equation