CSE 144 (Section Id: 420441)
Computer-Aided Design of VLSI Circuits
Professor Alex Orailoglu
4840 AP&M
Tel: 858-534-0914 Fax: 858-534-7029
email: alex@cs.ucsd.edu

Course Contents


**Silicon Compilation**  Time-Constrained Scheduling. Allocation Tasks. Functional Unit Binding. Storage Unit Binding. Interconnection Binding.